

## Oversampled A/D & D/A Converters

1. J.C. Candy and G.C. Temes, "Oversampling Methods for A/D and D/A Conversion," pp. 1-29, IEEE Press, 1992.
2. S. R. Norsworthy, R. Schreier, and G. C. Temes, "Delta-Sigma Data Converters, Theory, Design, and Simulation," IEEE Press, 1997.
3. B.E. Boser and B.A. Wooley, "The Design of Sigma-Delta Modulation A/D Converters," IEEE J. Solid-State Circuits, vol. 23, no. 6, pp. 1298-1308, Dec. 1988.
4. N.S. Ssooch et al., "18-Bit Stereo D/A Converter with Integrated Digital and Analog Filters," in digest 91 AES convention, New York, pp. 6-73/6-84, October 1991.
5. K.C.H. Chao et al., "A High-Order Topology for Interpolative Modulators for Oversampling A/D Converters," IEEE Transactions on Circuits and Systems," vol. 37 no. 3, pp. 309-318, March 1990.
6. R. Gray, "Spectral analysis of quantization noise in a single-loop sigma-delta modulator with dc input," *IEEE Trans. Commun.*, vol. 37, pp. 588-599, June 1989.
7. B. P. Brandt, D. E. Wingard, and B. A. Wooley, "Second-order sigma-delta modulation for digital-audio signal acquisition," IEEE Journal of Solid-State Circuits, vol. 26, pp. 618 - 627, April 1991.
8. B.P. Brandt et al., "A 50-MHz Multibit Sigma-Delta Modulator for 12-b 2-MHz A/D Conversion," IEEE J. Solid-State Circuits, vol. 26, no. 12, pp. 1746-1756, Dec. 1991.
9. B.P. Brandt et al., "A Low-Power, Area Efficient Digital Filter for Decimation and Interpolation," IEEE J. Solid-State Circuits, vol. 29, no. 6, pp. 679-687, June 1994.
10. T.L. Brooks et al., "A Cascaded Sigma-Delta Pipeline A/D Converter with 1.25 MHz Signal Bandwidth and 89 dB SNR," IEEE Journal of Solid-State Circuits, vol. 32, no. 12, pp. 1896-1906, December 1997.
11. E.J. van der Zwan et al., "A 0.2mW CMOS  $\Sigma\Delta$  Modulator for Speech Coding with 80dB Dynamic Range," IEEE J. Solid-State Circuits, vol. 31, no. 12, pp. 1873-1880, Dec. 1996.

12. D.K. Su et al., "A CMOS Oversampling D/A Converter with a Current-Mode Semidigital Reconstruction Filter," *IEEE J. Solid-State Circuits*, pp. 1224-1233, Dec. 1993.
13. R. E. Crochiere and L. R. Rabiner, "Interpolation and Decimation of Digital Signals – A Tutorial Review", *Proc. IEEE*, 69, pp. 300-331, March 1981.
14. Eugene Hogenauer, "An Economical Class of Digital Filters for Decimation and Interpolation", *IEEE Trans. Acoustics, Speech, and Signal Processing*, ASSP-29, April 1981.
15. P. Vaidyanathn and T. Q. Nguyen, "A 'Trick' for the Design of FIR Half-band Filters", *IEEE Trans. Circuits Sys.*, CAS-34, pp. 297-300, March 1987.